

Name: \_\_\_\_\_

MODEL ANSWER

Matric No: \_\_\_\_\_ Section: \_\_\_\_\_



جامعة إسلامية عالمية ماليزية

INTERNATIONAL ISLAMIC UNIVERSITY MALAYSIA

MID-TERM EXAMINATION  
SEMESTER II, 2012/2013 SESSION  
KULLIYYAH OF ENGINEERING

Programme : ENGINEERING Level of Study : UG 2

Time : 8:00pm-10:00 pm Date : 22/03/2013

Duration : 2 Hours

Course Code : ECE 2133 Section(s) : 1 & 2

Course Title : Electronic Circuits

This Question Paper consists of Seven (7) Printed Pages (Including Cover and a blank page) with Two (2) Questions.

INSTRUCTION(S) TO CANDIDATES

DO NOT OPEN UNTIL YOU ARE ASKED TO DO SO

- Use only pen for writing answer.
- Do not use your own sheet.
- Useful formulas and values are given in page 7.
- A total mark of this examination is 60.
- This examination is worth 30% of the total assessment.
- For drawing you may use pencil
- Answer ALL questions.

Any form of cheating or attempt to cheat is a serious offence which may lead to dismissal.

	Question 1a	Question 1b	Question 2a	Question 2b	Total Marks
Marks	15	15	15	15	60
Marks Obtained					

## Q.1 [30 marks]

- (a) Determine the Q-point values ( $I_{DQ}$  and  $V_{DSQ}$ ) for the circuit shown in Fig. 1(a). The transistor parameters are  $V_{TN} = 1.2$  V,  $K_n = 0.5$  mA/V<sup>2</sup> and  $\lambda = 0$ . Also calculate the small signal circuit parameters.

(15 marks)

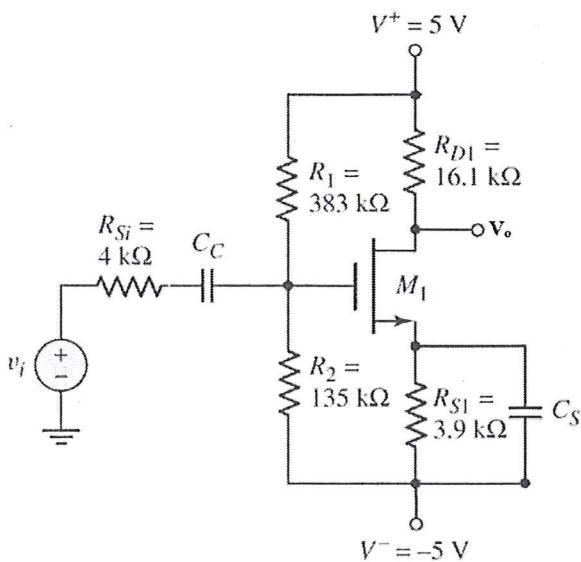
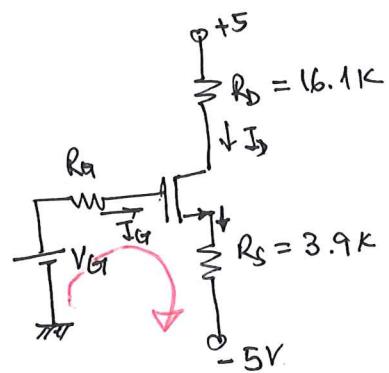


Fig. 1(a)

$$R_Q = R_1 \parallel R_2 = 383 \parallel 135 = 99.8167 \text{ k}\Omega$$



$$\begin{aligned} 5 &= I_D (R_D + R_S) + V_{DS} - 5 \\ \therefore V_{DS} &= 5 + 5 - I_D (R_D + R_S) \\ &= 10 - 0.19845 \times 20 \\ &= 6.031 \text{ V} \end{aligned}$$

$$V_G = \frac{(5+5) \times 135}{135 + 383} \times 135 - 5 = \frac{10 \times 135}{518} - 5 = -2.394 \text{ V}$$

$$V_G = I_{GQ} R_Q + V_{GS} + R_S I_S - 5 = V_{GS} + R_S K_n (V_{GS} - V_T)^2 - 5$$

$$-2.394 = V_{GS} + 3.9 \text{ k} \times 0.5 \text{ mA/V}^2 (V_{GS} - 1.2)^2 - 5$$

$$= V_{GS} + 1.95 (V_{GS}^2 - 2.4 V_G + 1.44) - 5$$

$$= 1.95 V_{GS} - 3.68 V_{GS} + 2.808 - 5$$

$$\therefore 1.95 V_{GS} - 3.68 V_{GS} + 0.202 = 0$$

$$V_{GS} = 1.83 \text{ V}, 0.056 \text{ V}$$

$$\text{choosing } V_{GS} = 1.83 > V_T$$

$$g_m = 2\sqrt{K_n I_{DQ}} = 2\sqrt{0.5 \times 0.19845} = 0.63 \text{ mA/V}$$

$$\begin{aligned} I_D &= K_n (V_{GS} - V_T)^2 = 0.5 (1.83 - 1.2)^2 \\ &= 0.19845 \text{ mA} \end{aligned}$$

$$r_o = \frac{1}{g_m I_{DQ}} = \infty$$

- (b) For the circuit shown in Fig 1(b), design a bias stable circuit such that  $I_{CQ} = 0.8 \text{ mA}$  and  $V_{CEQ} = 5.0 \text{ V}$  assuming  $\beta=100$ . Also find small signal parameters  $g_m$ ,  $r_\pi$  and  $r_o$  if  $V_A = 100$ . [Hints: determine the values of  $R_C$ ,  $R_1$  and  $R_2$ ] **(15 marks)**

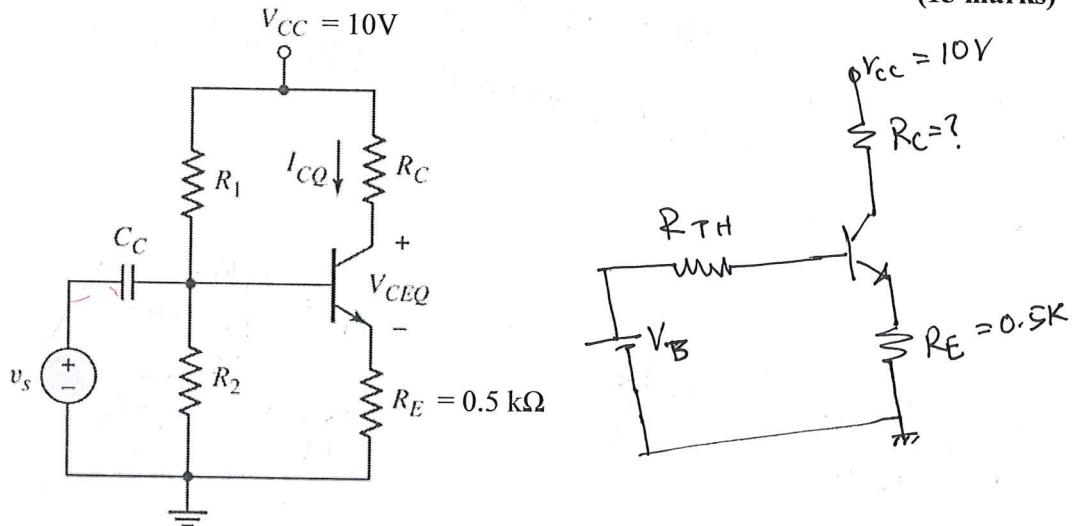


Fig. 1 (b)

$$\text{For bias stable, } R_{TH} = R_B = 0.1(1+\beta) \times R_E = 0.1(1+100) \times 0.5 \text{ k}\Omega = 5.05 \text{ k}\Omega$$

$$\begin{aligned} V_{CC} &= I_c R_c + V_{CEQ} + I_E R_E \\ &= I_c R_c + V_{CEQ} + (1+\beta) I_B R_E \\ \therefore I_c R_c &= V_{CC} - V_{CEQ} - \frac{(1+\beta) I_c R_E}{\beta} \\ &= 10 - 5 - \frac{101}{100} \times 0.5 \times 0.8 \text{ mA} \\ &= 4.596 \text{ mA} \end{aligned}$$

$$\therefore R_c = 5.745 \text{ k}\Omega \quad \star$$

$$\begin{aligned}
 V_B &= I_B R_B + V_{BE} + I_E R_E \\
 &= \frac{I_C}{\beta} R_B + V_{BE} + \frac{(1+\beta)}{\beta} I_C R_E \\
 &= \frac{0.8}{100} \times 5.05 + 0.7 + \frac{101 \times 0.8 \times 0.5}{100} \\
 &= 0.0404 + 0.7 + 0.404 = 1.1444 \text{ V} \leftarrow
 \end{aligned}$$

$$R_B = \frac{R_1 R_2}{R_1 + R_2}$$

$$V_B = \frac{V_C R_2}{R_1 + R_2} = \frac{V_C R_B}{R_1}$$

$$\frac{R_B}{R_1} = \frac{R_2}{R_1 + R_2}$$

$$\begin{aligned}
 \therefore R_1 &= \frac{V_C \cdot R_B}{V_B} \\
 &= \frac{10}{1.1444} \times 5.05 \text{ k} \Omega = 44.128 \text{ k} \Omega \leftarrow
 \end{aligned}$$

$$R_2 = \frac{1}{\frac{1}{R_B} - \frac{1}{R_1}} = 5.70 \text{ k} \Omega \leftarrow$$

$$\frac{1}{R_B} = \frac{1}{R_1} + \frac{1}{R_2}$$

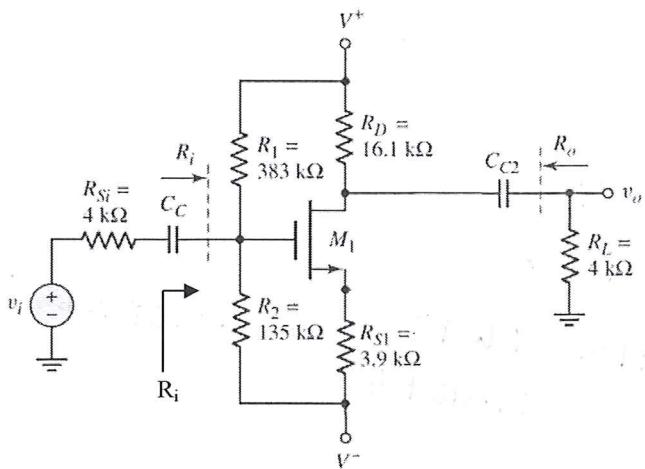
$$\begin{aligned}
 g_m &= \frac{I_{CQ}}{0.026} = \frac{0.8}{0.026} = 30.77 \text{ mA/V} \\
 r_{\pi} &= \frac{\beta}{g_m} = \frac{100}{30.77} = 3.25 \text{ k} \Omega \leftarrow
 \end{aligned}$$

$$\beta = g_m r_{\pi}$$

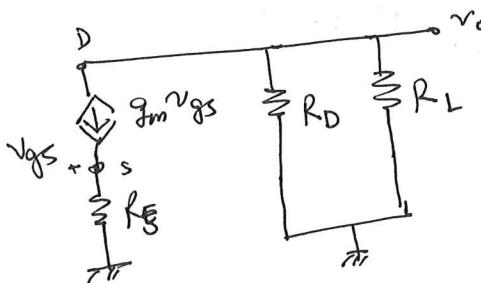
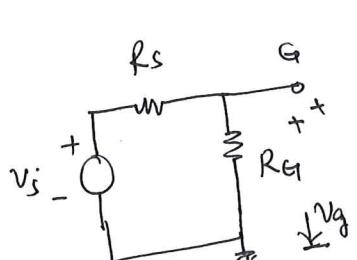
$$r_o = \frac{V_A}{I_{CQ}} = \frac{80}{0.8} = 100 \text{ k} \Omega \leftarrow$$

**Q.2 [20 marks]**

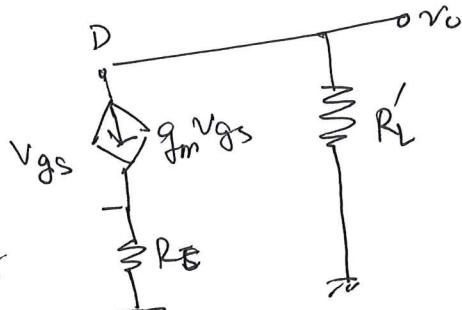
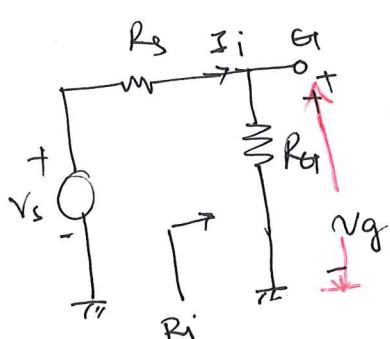
- (a) Draw the small signal equivalent circuit diagram of the MOSFET amplifier circuit shown in Fig. 2(a) and find the input resistance  $R_i$  and the voltage gain  $A_v = \frac{v_o}{v_s}$  of the amplifier. The transistor parameters are  $g_m = 0.65 \text{ mA/V}$  and  $\lambda = 0$ . (15 marks)



$$\lambda = 0 \quad r_o = \infty$$



$$R_i = \frac{v_g}{I_i} = \frac{R_G I_i}{I_i} = R_G = R_1 \parallel R_2 = 99.816 \text{ k}\Omega$$



$$v_o = -g_m v_{gs} \times R'_L$$

$$v_g = v_{gs} + g_m v_{gs} R_E \\ = v_{gs} (1 + g_m R_E)$$

$$\therefore A_{VA} = \frac{v_o}{v_g} = \frac{-g_m v_{gs} R'_L}{v_{gs} (1 + g_m R_E)}$$

$$= -\frac{g_m R'_L}{1 + g_m R_E}$$

$$A_{v_A} = \frac{-g_m R'_L}{1 + g_m R_S}$$

$$R'_L = 16.1 // 4 \\ = 3.204 \text{ K}\Omega$$

$$= \frac{-0.69 \times 3.204}{1 + 0.69 \times 3.9} = -0.5891$$

$$\begin{aligned} A_V &= \frac{V_o}{V_s} = A_{v_A} \times \frac{R_i}{R_i + R_S} \\ &= -0.5891 \times \frac{99.816}{99.816 + 4} = -0.5664 \end{aligned}$$

$$R_i = 99.816 \text{ K}\Omega \quad \leftarrow$$

- (b) Draw the small signal equivalent circuit diagram of the BJT amplifier circuit shown in Fig. 2(b) and find the current gain  $A_i = \frac{I_o}{I_i}$  and output resistance  $R_o$

where  $I_o$  is the current flowing through  $R_L$ . Given that  $R_1 = 300 \text{ k}\Omega$ ,  $R_2 = 500 \text{ k}\Omega$ ,  $R_E = 20.0 \text{ k}\Omega$  and  $I_D = 0.25 \text{ mA}$ . The transistor parameters are  $\beta = 100$ ,  $V_{BE(on)} = 0.7 \text{ V}$  and  $V_A = 80$ . (15 marks)

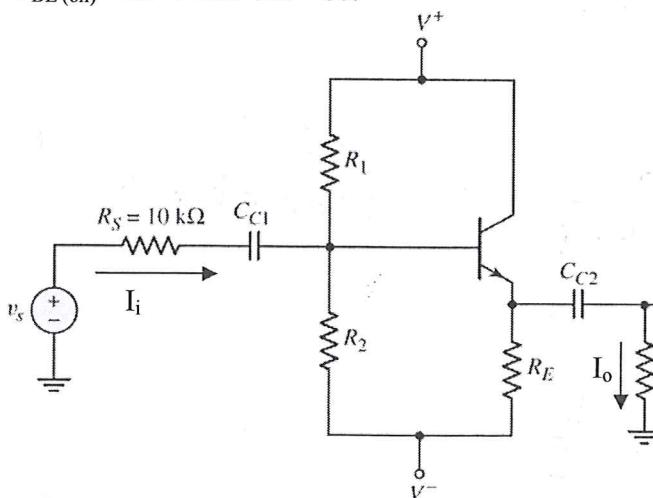
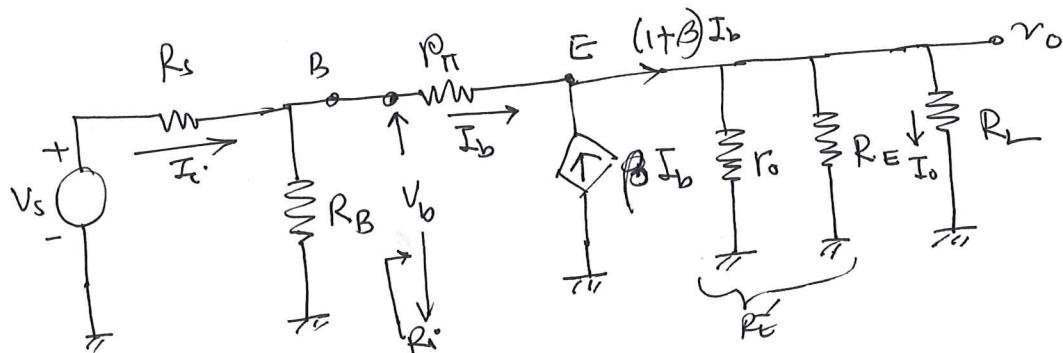


Fig. 2(b)

$$R_o = \frac{V_A}{I_C} = \frac{80}{0.25 \text{ mA}} = 320 \text{ k}\Omega \quad \checkmark$$

$$r_{\pi} = \frac{\beta V_T}{I_C} = \frac{100 \times 0.026}{0.25} = 10.4 \text{ k}\Omega \quad \checkmark$$

$$R'_L = r_o \parallel R_E \parallel R_L = 320 \parallel 20 \parallel 5 = 3.95 \text{ k}\Omega$$



$$R'_E = r_o \parallel R_E = 320 \parallel 20 = 18.82 \text{ k}\Omega$$

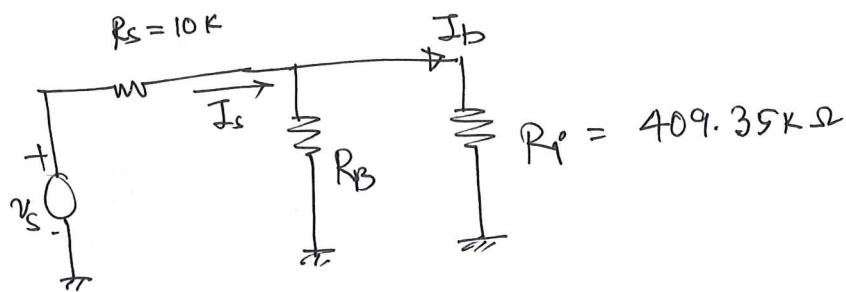
$$I_o = \frac{I_b(1+\beta) \times R'_E}{R_e + R'_E} = 10 \times 18.82 \times \frac{10.4}{10.4 + 18.82} = 79.78 \text{ mA}$$

$$R_i = \frac{V_b}{I_b} = \frac{r_{\pi} I_b + I_b(1+\beta) R'_L}{\beta I_b} = r_{\pi} + (1+\beta) R'_L$$

$$= 10.4 + 101 \times 3.95$$

$$= 409.35 \text{ k}\Omega \quad \checkmark$$

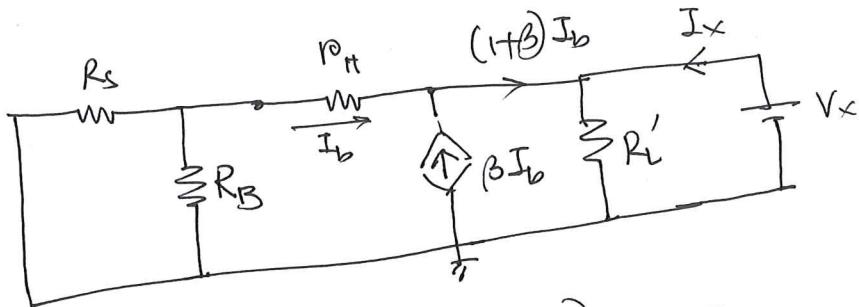
$$R_B = \frac{R_1 \parallel R_2}{= 300 \parallel 500} = 187.5 \text{ k}\Omega$$



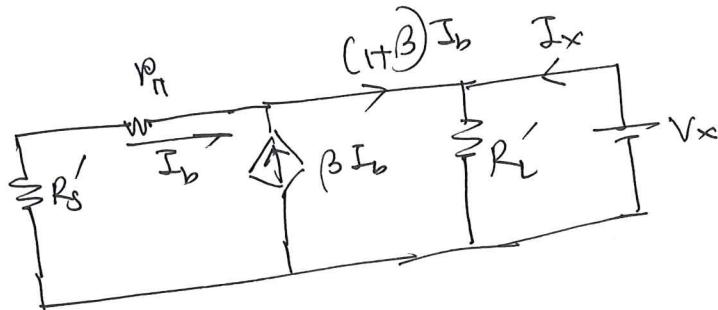
$$\frac{I_B}{I_s} = \frac{R_B}{R_B + R_o} = \frac{187.5}{187.5 + 409.35} = 0.3141$$

$$\therefore A_{I^2} = \frac{I_o}{I_s} = \frac{I_o}{I_B} \cdot \frac{I_B}{I_s}$$

$$= 79.787 \times 0.3141 = 25.065 \quad \cancel{A}$$

$R_o = ?$ 

$$\begin{aligned}
 R'_L &= R_B \parallel R_S \\
 &= 187.5 \parallel 10 \\
 &= 9.494 \text{ k}\Omega
 \end{aligned}$$



$$R_o = 10.4 \text{ k}\Omega$$

$$I_x = \frac{V_x}{R'_L} - (1+\beta) I_B$$

$$I_B = -\frac{V_x}{r_{\pi} + R'_L}$$

$$\begin{aligned}
 &= \frac{V_x}{R'_L} + \frac{(1+\beta) V_x}{r_{\pi} + R'_L} = \frac{V_x}{R'_L} + \frac{V_x}{\frac{r_{\pi} + R'_L}{(1+\beta)}}
 \end{aligned}$$

$$\therefore \frac{1}{R_o} = \frac{1}{R'_L} + \frac{1}{\frac{r_{\pi} + R'_L}{(1+\beta)}}$$

$$\therefore R_o = R'_L \parallel \left( \frac{r_{\pi} + R'_L}{1+\beta} \right)$$

$$\therefore \frac{r_{\pi} + R'_L}{1+\beta} = \frac{10.4 + 9.494}{1+100} = 0.19696 \text{ k}\Omega$$

$$\begin{aligned}
 \therefore R_o &= 3.95 \parallel 0.19696 \text{ k}\Omega = 0.1876 \text{ k}\Omega \\
 &= 187.6 \parallel \Omega \quad \leftarrow
 \end{aligned}$$

**Useful formulas****For BJT**For bias stable  $R_B = 0.1(1 + \beta)R_E$ 

$$g_m = \frac{I_{CQ}}{V_T}$$

$$r_\pi = \frac{\beta V_T}{I_{CQ}}$$

$$r_o = \frac{V_A}{I_{CQ}}$$

$$V_T = 0.026 \text{ V}$$

$$V_{BE(on)} = 0.7 \text{ V}$$

**For MOSFET**

$$g_m = 2\sqrt{k_n I_{DQ}}$$

$$r_o = \frac{1}{\lambda I_{DQ}}$$

*Large values of capacitors are connected to the circuits as coupling and bypass capacitors.*